



Please type a plus sign (+) inside this box → +

PTO/SB/08A (08-00)

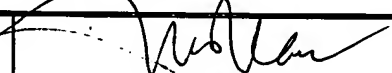
Approved for use through 10/31/2002. OMB 0651-0031  
U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number

<p>Substitute for form 1449A/PTO</p> <p><b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b></p> <p><i>(use as many sheets as necessary)</i></p>		<b>Complete if Known</b>	
Application Number		10/720,857	
Filing Date		November 24, 2003	
First Named Inventor		Pravas Pradhan et al.	
Group Art Unit		2816	
Examiner Name		Not yet assigned	
Attorney Docket Number		112055-0073U	

Sheet	2	of	2
-------	---	----	---

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
n	7.	SEEVINCK, Evert, Current-Mode Techniques for High-Speed VLSI Circuits with to Current Sense Amplifier for CMOS SRAM's, IEEE Journal of Solid-State Circuits, Vol. 26, No. 4, April 1991	
n	8.	SIM, Jae-Yoon, A 1-Gb/s Bidirectional I/O Buffer Using the Current-Mode Scheme, IEEE Journal of Solid-State Circuits, Vol. 34, No. 4, April 1999	

Examiner Signature 	Date Considered	12/7/04
---	--------------------	---------

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number. <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.